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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10050104	FILING DATE 01/18/2002	CLASS 438	SUBCLASS 217 ?	GAU 2812	EXAMINER Bratliekson
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\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

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PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO SEC.890
TITLE : Self-contained semiconductor device manufacturing equipment having compact arrangement of load-lock and processing chambers		
U.S.DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING Sheets Drwg. Figs.Drwg. Print Fig.
Amount Due	Date Paid	PREPARED FOR ISSUE	Application Examiner
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